

**REMARKS/ARGUMENTS**

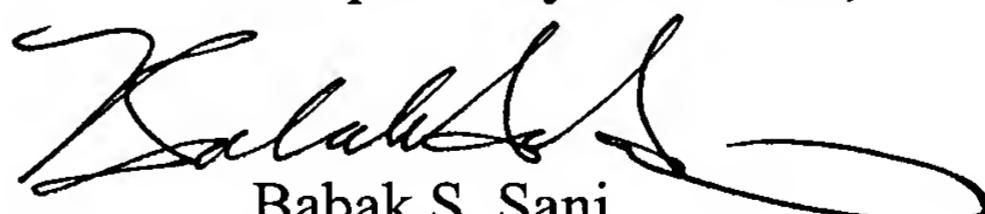
By this amendment applicant has amended the specification consistent with amendments made in the parent application number 10/038,737. Further amendments to the specification have been made to the "Brief Description of the Drawings" and the "Detailed Description of the Invention" to incorporate description from related, commonly-assigned U.S. patent application number 10/037,861, entitled "Self-Compensating Delay Chain for Multiple Data-Rate Interfaces," by Chong et al., which was incorporated by reference in the instant application. The instant application incorporates by reference the related application generally at page 1 and more specifically at page 6, where reference is made to various embodiments of phase control circuitry that have been described in detail in the related application. It is the description for one of the embodiments of the phase control circuitry shown in Figure 7 of the related application that has been reproduced in the instant application. Applicant submits that no new matter has been introduced by this amendment.

**CONCLUSION**

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-576-0200.

Respectfully submitted,



Babak S. Sani  
Reg. No. 37,495

TOWNSEND and TOWNSEND and CREW LLP  
Two Embarcadero Center, Eighth Floor  
San Francisco, California 94111-3834  
Tel: 415-576-0200 / Fax: 415-576-0300  
Attachments  
BSS:deb 60061463 v1